



UK Semiconductor Advisory Panel Meeting #10 Minutes

17/09/25 11:30 at 22-26 Whitehall, London, SW1A 2EG

Attendees

Panel Members Attending

- Minister Patrick Vallance (Co-Chair)
- Dr Jalal Bagherli (Co-Chair)
- Prof John Goodenough
- Dr Andy Sellars
- Pete Magowan
- Jen Walls
- Rebecca Dobson (Online)
- Jutta Meier (Online)
- Scott White (Online)
- Andrew Evans (Online)
- Richard Grisenthwaite (Online)

Guests

- Nick McKeown – Council of Science and Technology
- George Mills – British Business Bank
- Stew Edmonson – UK Electronic Skills Foundation

HMG attendees:

- Dr Dave Smith – National Technology Advisor
- Dr John Goodacre – Director of Secure and Resilience Growth, Innovate UK
- Policy Lead - MOD
- Deputy Director of Semiconductor Team
- Joint Head of Semiconductor Team
- Joint Head of Semiconductor Team
- Senior Policy Advisor – Semiconductors
- Private Secretary

Summary

- The Panel discussed the DSIT Semiconductor Unit's Semiconductor Action Plan, measures of success, and how to ensure this plan delivers the right outcomes.
- The Panel discussed recommendations around building a sovereign AI industry, and the trade-offs involved.
- Presentations from BBB and UKESF gave wider context on initiatives DSIT and the wider government are involved in.
- The Panel gave input into future skills initiatives and how they could be developed.
- The Panel discussed the DSIT's wider work on semiconductors, with a forward look of what to expect in the coming year.