



UK Semiconductor Advisory Panel Meeting #9 Minutes

14/07/25 10:30am at 100 Parliament Street, London, SW1A 2BQ

Attendees

Panel members attending

- Minister Patrick Vallance (Co-Chair)
- Dr Jalal Bagherli (Co-Chair)
- Janet Collyer
- Rebecca Dobson
- Andrew Evans
- Prof John Goodenough (online)
- Richard Grisenthwaite
- Pete Magowan
- Jutta Meier
- Dr Andy Sellars
- Scott White
- Jen Walls (online)

Guests

- Nick McKeown – Council for Science and Technology

HMG attendees:

- Dr Dave Smith – National Technology Advisor
- Joint Head of Semiconductor Team
- Joint Head of Semiconductor Team
- Policy Lead – Semiconductors
- Policy Lead – Semiconductors
- Senior Policy Advisor – Semiconductors
- Policy Advisor – Semiconductors
- Policy Advisor – Semiconductors
- Private Secretary

Summary

- The Panel were briefed on the inclusion of semiconductors in the Industrial Strategy, as a frontier technology in the Digital and Technologies Sector plan and an underpinning technology of other growth driving sectors.
- The Panel considered a presentation on Building a Sovereign AI Chip Design Industry and gave steers on balancing a focus on AI as the primary growth driving sector for semiconductors with other areas of UK strength.
- The Panel gave steers on DSIT's international approach to semiconductors, emphasising the need to better leverage consulates, and the importance of talent and skills in international partnerships.
- The Panel discussed DSIT's hardware security activities and promotion of CHERI.
- DSIT updated the Panel on the restructuring of the Semiconductor Team to enable delivery of activities in Spending Review Phase I and the Industrial Strategy; and gave a forward look of activities.
- The Panel repeatedly emphasised the importance of cross-government coordination to leverage the connection to semiconductors throughout the Industrial Strategy, particularly in defence, and clean energy.