



UK Semiconductor Advisory Panel Meeting #8 Minutes

03/04/25 14:00 at 100 Parliament Street, London, SW1A 2BQ

Attendees

Panel Members Attending

- Minister Patrick Vallance (Co-Chair)
- Dr Jalal Bagherli (Co-Chair)
- Janet Collyer
- Prof John Goodenough
- Dr Andy Sellars
- Scott White (Online)
- Richard Grisenthwaite (Online)
- Andrew Evans
- Professor Rachel Oliver
- Pete Magowan
- Jen Walls

Guests

- Nick McKeown (online) – Council of Science and Technology

HMG attendees:

- Dr Dave Smith – National Technology Advisor
- Gary Cutts – Executive Director for Digital and Technologies, Innovate UK
- Deputy Director of Semiconductor Team
- Joint Head of Semiconductor Team
- Joint Head of Semiconductor Team
- Policy Lead – Semiconductors
- Policy Lead – Industrial Strategy
- Senior Policy Advisor
- Private Secretary

Summary

- The Panel discussed how to ensure that a government-led strategic semiconductor innovation programme can be most impactful and best deliver for industry.
- The Panel gave steer on the deployment of a coordinating National Semiconductor Centre, and how it could be work within existing government-funded institutions in the semiconductor sector
- The Panel discussed the interplay between OxCam Corridor and the semiconductor sector.
- The Panel discussed DSIT's wider work on semiconductors, with a forward look of what to expect in the coming year.
- The Panel considered a presentation on the Industrial Strategy's Digital and Technologies Sector Plan, and discussed how this can best support the semiconductor sector.