



## **UK Semiconductor Advisory Panel Meeting #7 Minutes**

27/11/24 11:15 at 100 Parliament Street, London, SW1A 2BQ

### **Attendees**

#### *Panel Members Attending*

- Minister Patrick Vallance (Co-Chair)
- Dr Jalal Bagherli (Co-Chair)
- Janet Collyer
- Prof John Goodenough
- Dr Andy Sellars
- Scott White
- Richard Grisenthwaite

#### *HMG attendees:*

- Andrew Tyrer – Innovate UK
- Dr Dave Smith – National Technology Advisor
- Deputy Director of Semiconductor Team
- Joint Head of Semiconductor Team
- Joint Head of Semiconductor Team
- Policy Lead
- Senior Policy Advisor
- Private Secretary

### **Summary**

- The Panel discussed semiconductor strategy in the context of the Industrial Strategy and how government can make meaningful interventions to deliver growth in the sector.
- The Panel discussed the shape and structure of how a proposed Semiconductor Centre could best make a difference in the UK.
- The Panel discussed potential avenues for R&D collaboration with the US and how to ensure mutually beneficial outcomes.