Department for Science, Innovation, & Technology

OFFICIAL

UK Semiconductor Advisory Panel Meeting #3 Minutes

17/11/23 10:30 at CSA Catapult, Imperial Park, Celtic way, Newport, NP10 8BE

Attendees

Panel Members Attending

- Tamsin Heath (Deputised Co-Chair)
- Dr Jalal Bagherli (Co-Chair)
- Amelia Armour
- Janet Collyer
- Rae Hyndman
- Prof John Goodenough (Virtual)
- Richard Grisenthwaite
- Americo Lemos
- Dr Andy Sellars
- Dr Eben Upton (Virtual)
- Scott White

Virtual Attendees from the Institute for Manufacturing

- Zongshuai Fan
- David Leal Ayala
- Edmund Linfield
- Nicky Athanassopoulou
- Michael Palladino

DSIT Secretariat attendees:

- Joint Head of Semiconductor Team
- Senior Policy Advisor
- Senior Policy Advisor
- Senior Policy Advisor
- Policy Advisor

Summary

- DSIT updated on ongoing work to deliver the National Semiconductor Strategy.
- The Panel discussed the economic analysis of the UK semiconductor industry, which
 is being conducted by Perspective Economics, suggesting that a breakdown of the
 current skills and roles landscape would be valuable as part of this work.
- The Panel discussed the UK Telecoms Innovation Network Working Group (UKTIN EWG), emphasising the need for cross-collaboration between technologies.
- The Panel discussed the UK government's steps to enhance the competitiveness of domestic semiconductor manufacturing, welcoming that existing funding was being made available to the industry but highlighting the need to support funder's understanding of the industry.
- The Panel discussed the eFutures program and what can be learnt from other coordination efforts.
- The session concluded with a deep dive on the draft recommendations from the Infrastructure Initiative study. The Panel advised that while there was a need for a body to provide a coordination function for the UK semiconductor sector, HMG should ensure it doesn't duplicate existing initiatives.