

## UK Semiconductor Advisory Panel - Terms of Reference

### Context

The UK's National Semiconductor Strategy, published in May 2023, aims to grow the UK semiconductor sector, safeguard the UK against supply chain disruption, and protect the UK against the security risks arising from semiconductor technologies. The Strategy announced the creation of the UK Semiconductor Advisory Panel in order to provide a formal conduit for industry and government to work closely together to deliver the goals of the Strategy.

### Objectives

The UK Semiconductor Advisory Panel aims to:

- Provide advice and guidance on implementation of the National Semiconductor Strategy;
- Co-design policies and programmes to deliver the Strategy and support the UK semiconductor sector;
- Encourage industry alignment and action towards the goals outlined in the Semiconductor Strategy;
- Monitor and steer actions to implement the Strategy taken by industry, academia and government;
- Promote synergy with other UK technology priorities and downstream user sectors (e.g., quantum, telecoms, AI, net zero, defence).

### Membership

The Advisory Panel is made up of a Ministerial co-chair, Saqib Bhatti MP, Minister for Tech and the Digital Economy, an industry co-chair, Dr Jalal Bagherli, and a small group of core panel members. When selecting the Advisory Panel, the co-chairs considered representation across the semiconductor supply chain, UK domestic strengths, geographical locations and company size. Members have been invited to join the Advisory Panel in their personal capacity and not to act on behalf of any organisation they are affiliated with. Additional experts will be invited to contribute on areas of their expertise on an ad-hoc basis.

Members of the UK Semiconductor Advisory Panel are:

- **(Panel Co-Chair) Saqib Bhatti MP** - Minister for Tech and the Digital Economy
- **(Panel Co-Chair) Dr Jalal Bagherli** - Chairman, PTSL; Chair, Williams Advanced Engineering
- **Amelia Armour** - Partner, Amadeus Capital Partners
- **Janet Collyer** - Chair, Quantum Dice and Machine Discovery; Senior Independent Director, EnSilica; Independent NED, Aerospace Technology Institute
- **Prof. John Goodenough** - Chair in Microelectronic Systems, University of Sheffield
- **Richard Grisenthwaite** - Executive VP & Chief Architect, Arm

- **Rae Hyndman** - CEO, Clas-SiC Wafer Fab
- **Americo Lemos** - CEO, IQE
- **Dr Andy Sellars** - Strategy Development Officer, Compound Semiconductor Applications Catapult
- **Dr Eben Upton** - CEO, Raspberry Pi
- **Scott White** - Executive Director, PragmatlC

## **Approach**

The co-chairs and Advisory Panel members will use their experience and contacts as leverage to bring wider stakeholders together to accelerate and increase the impact of existing and future interventions in support of the National Semiconductor Strategy.

Meetings will address progress made against the Strategy objectives. In addition, each meeting will focus on a topic of importance to the UK semiconductor sector and the goals of the Strategy. These may include:

- Skills and the talent pipeline
- UK Semiconductor Infrastructure Initiative
- R&D road mapping
- Support for semiconductor manufacturing
- International collaboration (inc. R&D partnerships & supply chains)
- Access to finance
- Spin-out and start-up ecosystem
- Growth stage / scale-up ecosystem
- Supply chain resilience & transparency
- Hardware security (inc. RISC-V & DSbD)

## **Governance**

The co-chairs are jointly responsible for the delivery of the Advisory Panel. The co-chairs will be supported by a secretariat based in the Department for Science, Innovation and Technology. The secretariat will be responsible for arranging meetings, circulating papers and recording minutes.

The secretariat will be overseen by a senior civil servant (Deputy Director) with responsibility for ensuring the co-chairs and members of the Advisory Panel have the resources needed, and that the meetings are run effectively.

Agendas and papers will be circulated at least 3 working days in advance of meetings. Minutes will be circulated and agreed within 2 weeks of meetings.

## **Process**

The Advisory Panel will convene in-person approximately every 2 months. These meetings will take place in London and in regional semiconductor clusters where

possible. Additional ad-hoc meetings on particular, time-sensitive, issues may also be called.

The secretariat will monitor the effectiveness of the Advisory Panel in meeting its intended objectives and will regularly review with the co-chairs whether there is a need to make changes to the structure, frequency and purpose of the meetings.

The Advisory Panel will initially run for a period of 18 months before being reviewed by the Secretary of State regarding its continuation.