

2018 No. XXXX

EXITING THE EUROPEAN UNION

DESIGNS

**The Design Right (Semiconductor Topographies) (Amendment)
(EU Exit) Regulations 2018**

Made - - - - *******

Laid before Parliament *******

Coming into force in accordance with regulation 1

The Secretary of State, in exercise of the powers conferred by section 7 of, and paragraph 16(b) of Schedule 7 to, the European Union (Withdrawal) Act 2018(a), makes the following Regulations:

Citation and commencement

1. These Regulations may be cited as the Design Right (Semiconductor Topographies) (Amendment) (EU Exit) Regulations 2018 and come into force on exit day.

Amendment to the Design Right (Semiconductor Topographies) Regulations 1989

2.—(1) The Design Right (Semiconductor Topographies) Regulations 1989(b) are amended as follows.

(2) In regulation 4 (qualification)—

- (a) in paragraph (2), in paragraph (b) in the words in quotation marks, for “another member State” substitute “a member State”;
- (b) in paragraph (4), —
 - (i) in paragraph (a) in the words in quotation marks, for “every member State of the European Economic Community” substitute “the United Kingdom”;
 - (ii) in paragraph (b) in the words in quotation marks, for “the territory of any member State” substitute “the United Kingdom”;
 - (iii) omit the words from “and subsection (4)” to the end.

(a) 2018 c.
(b) SI 1989/1100, amended by S.I. 2006/1833 and S.I. 2008/1434.

Name

Date

Department for Business, Energy and Industrial Strategy

EXPLANATORY NOTE

(This note is not part of the Regulations)

These Regulations amend the Design Right (Semiconductor Topographies) Regulations 1989 in order to address failures of these Regulations to operate effectively arising from the withdrawal of the United Kingdom from the European Union. The provisions which are amended concern the conditions under which a semiconductor topography qualifies for design right protection under Part 3 of the Copyright, Designs and Patents Act 1988 (c.48).